



# DNLS320A

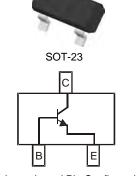
### LOW V<sub>CE(SAT)</sub> NPN SURFACE MOUNT TRANSISTOR

#### Features

- Epitaxial Planar Die Construction
- Ideal for Medium Power Amplification and Switching
- Complimentary PNP Type Available (DPLS320A)
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

## Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.008 grams (approximate)



Schematic and Pin Configuration

Maximum Ratings	$@T_A = 25^{\circ}C$ unless otherwise specified
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Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	20	V
Collector-Emitter Voltage	V <sub>CEO</sub>	20	V
Emitter-Base Voltage	V <sub>EBO</sub>	5	V
Peak Pulse Current	I <sub>CM</sub>	5	А
Repetitive Peak Pulse Current (Note 3)	I <sub>CRP</sub>	3	А
Continuous Collector Current	Ic	2	А
Base Current	Ι <sub>Β</sub>	0.5	А

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4) @ T <sub>A</sub> = 25°C	PD	600	mW
Thermal Resistance, Junction to Ambient Air (Note 3) @ T <sub>A</sub> = 25°C	R <sub>0JA</sub>	209	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes:

1. No purposefully added lead.

2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.

3. Operated under pulse conditions: Pulse width  $\leq$  100ms, duty cycle  $\leq$  0.25.

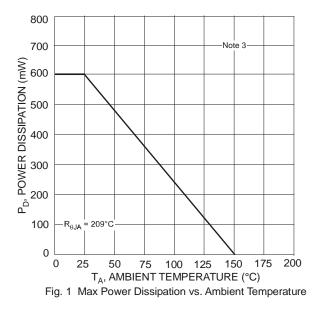
4. Device mounted on FR-4 PCB; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

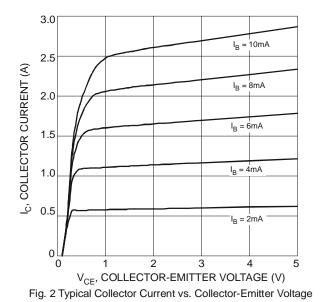


# **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

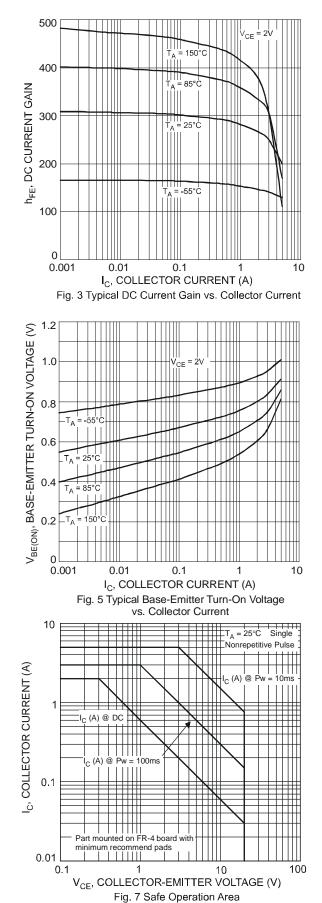
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
OFF CHARACTERISTICS (Note 5)				•		•
Collector Base Cutoff Current	1	_	_	100	nA	$V_{CB} = 20V, I_E = 0$
Collector-Base Cutoff Current	I <sub>CBO</sub>	_	_	50	μΑ	V <sub>CB</sub> = 20V, I <sub>E</sub> = 0, T <sub>A</sub> = 150°C
Emitter-Base Cutoff Current	I <sub>EBO</sub>	_	_	100	nA	$V_{EB} = 5V, I_{C} = 0$
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	20	_		V	I <sub>C</sub> = 100μA
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	20	_		V	I <sub>C</sub> = 10mA
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	5	_		V	I <sub>E</sub> = 100μA
ON CHARACTERISTICS (Note 5)						
		220	—			$V_{CE} = 2V, I_{C} = 0.1A$
		220				$V_{CE} = 2V, I_{C} = 0.5A$
DC Current Gain	h <sub>FE</sub>	220	—		—	$V_{CE} = 2V, I_C = 1A$
		200		—		$V_{CE} = 2V, I_C = 2A$
		150	_			$V_{CE} = 2V, I_C = 3A$
		_	_	70		$I_{C} = 0.5A, I_{B} = 50mA$
		_	_	120		$I_{C} = 1A, I_{B} = 50mA$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	_	230	mV	$I_{C} = 2A, I_{B} = 40mA$
		_	_	210		$I_{\rm C} = 2A, I_{\rm B} = 200 {\rm mA}$
		_	_	310		I <sub>C</sub> = 3A, I <sub>B</sub> = 300mA
Equivalent On-Resistance	R <sub>CE(SAT)</sub>	_	85	105	mΩ	$I_E = 2A, I_B = 200mA$
Base-Emitter Saturation Voltage		_	_	1.1	V	$I_{C} = 2A, I_{B} = 40mA$
Dase-Emilier Saturation voltage	VBE(SAT)	_	_	1.2	V	I <sub>C</sub> = 3A, I <sub>B</sub> = 300mA
Base-Emitter Turn-on Voltage	V <sub>BE(ON)</sub>			1.2	V	$V_{CE} = 2V, I_C = 1A$
SMALL SIGNAL CHARACTERISTICS						
Transition Frequency	f⊤	100	220	—	MHz	V <sub>CE</sub> = 5V, I <sub>C</sub> = 100mA, f = 100MHz
Output Capacitance	C <sub>ob</sub>	_		35	pF	$V_{CB} = 10V, f = 1MHz$

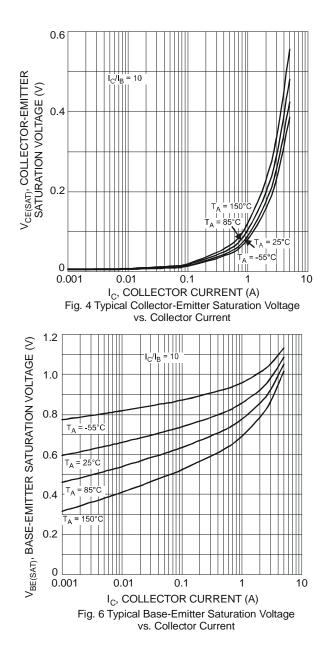
Notes: 5. Measured under pulsed conditions. Pulse width =  $300\mu$ s. Duty cycle  $\leq 2\%$ .











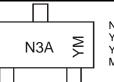


#### Ordering Information (Note 6)

Device	Packaging	Shipping
DNLS320A-7	SOT-23	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

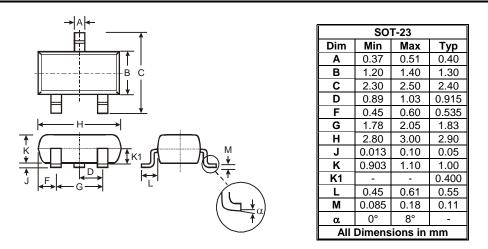
## **Marking Information**



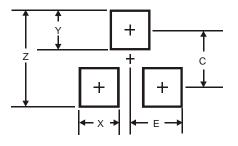
N3A = Product Type Marking Code YM = Date Code Marking Y = Year (ex: V = 2008) M = Month (ex: 9 = September)

Date Code Key												
Year	2008		2009	2010		2011	2012	2	2013	2014	•	2015
Code	V		W	Х		Y	Z		А	В		С
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

## **Package Outline Dimensions**



## **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35

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